



MODBUS

Communication Protocol

for energy meters with integrated RS485 interface

UEM45, UEM100, UEMCT, UEMRGW, UEM2L, UEM4L series

PROTOCOL MANUAL

Ed2505

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1. DESCRIPTION

MODBUS RTU is a master-slave communication protocol, able to support up to 247 slaves connected to a bus or a star network. The protocol uses a simplex connection on a single line. In this way, the communication messages move on a single line in two opposite directions.

Master-slave messages can be:

- Reading (Function codes \$03): the communication is between the master and a single slave. It allows to read information about the queried instrument
- Writing (Function code \$06, \$10): the communication is between the master and a single slave. It allows to change the instrument settings

In a multi-point type connection (MODBUS RTU), slave address (called also logical number) allows to identify each instrument during the communication. Each meter is preset with a default slave address (01) and the user can change it.

Communication frame structure - RTU mode

Bit per byte: 1 Start, 8 Bit, None, 1 Stop (8N1)

Name	Length	Function
START FRAME	4 chars idle	At least 4 character time of silence (MARK condition)
ADDRESS FIELD	8 bits	Instrument logical number
FUNCTION CODE	8 bits	Function code (\$03 / \$06 / \$10)
DATA FIELD	n x 8 bits	Data + length will be filled depending on the message type
ERROR CHECK	16 bits	Error check (CRC)
END FRAME	4 chars idle	At least 4 character time of silence between frames

1.1 CRC Generation

The Cyclical Redundancy Check (CRC) field is two bytes, containing a 16-bit value. The CRC value is calculated by the transmitting device, which appends the CRC to the message. The receiving device recalculates a CRC during receipt of the message, and compares the calculated value to the actual value it received in the CRC field. If the two values are not equal, an error results.

The CRC is started by first preloading a 16-bit register to all 1's. Then a process begins of applying successive 8-bit bytes of the message to the current contents of the register. Only the eight bits of data in each character are used for generating the CRC. Start and stop bits, and the parity bit, do not apply to the CRC.

During generation of the CRC, each 8-bit character is exclusive ORed with the register contents. Then the result is shifted in the direction of the least significant bit (LSB), with a zero filled into the most significant bit (MSB) position. The LSB is extracted and examined. If the LSB was a 1, the register is then exclusive ORed with a preset, fixed value. If the LSB was a 0, no exclusive OR takes place.

This process is repeated until eight shifts have been performed. After the last (eighth) shift, the next 8-bit character is exclusive ORed with the register's current value, and the process repeats for eight more shifts as described above. The final contents of the register, after all the characters of the message have been applied, is the CRC value.

A calculated procedure for generating a CRC is:

1. Load a 16-bit register with \$FFFF. Call this the CRC register.
2. Exclusive OR the first 8-bit byte of the message with the low-order byte of the 16-bit CRC register, putting the result in the CRC register.
3. Shift the CRC register one bit to the right (toward the LSB), zero-filling the MSB. Extract and examine the LSB.
4. (If the LSB was 0): Repeat Step 3 (another shift). (If the LSB was 1): Exclusive OR the CRC register with the polynomial value \$A001 (1010 0000 0000 0001).
5. Repeat Steps 3 and 4 until 8 shifts have been performed. When this is done, a complete 8-bit byte will have been processed.
6. Repeat Steps 2 through 5 for the next 8-bit byte of the message. Continue doing this until all bytes have been processed.
7. The final contents of the CRC register is the CRC value.
8. When the CRC is placed into the message, its upper and lower bytes must be swapped as described below.

Placing the CRC into the Message

When the 16-bit CRC (two 8-bit bytes) is transmitted in the message, the low-order byte will be transmitted first, followed by the high-order byte.

For example, if the CRC value is \$35F7 (0011 0101 1111 0111):

Addr	Func	Data Count	Data	Data		Data	CRC lo F7	CRC Hi 35
------	------	------------	------	------	------	------	-----------	-----------

CRC generation functions - With Table

All of the possible CRC values are preloaded into two arrays, which are simply indexed as the function increments through the message buffer. One array contains all of the 256 possible CRC values for the high byte of the 16-bit CRC field, and the other array contains all of the values for the low byte. Indexing the CRC in this way provides faster execution than would be achieved by calculating a new CRC value with each new character from the message buffer.

```
/*CRC table for calculate with polynom 0xA001 with init value 0xFFFF, High half word*/
rom unsigned char CRC_Table_Hi[] = {
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81,
    0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
    0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01,
    0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41,
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81,
    0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0,
    0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01,
    0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41,
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81,
    0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1,
    0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80,
    0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
    0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01,
    0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41,
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80,
    0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
    0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01,
    0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41,
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80,
    0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
    0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01,
    0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41,
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80,
    0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
    0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01,
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    0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
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    0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
    0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01,
    0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41,
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80,
    0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
    0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01,
    0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41,
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80,
    0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
    0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01,
    0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41,
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80,
    0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
    0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01,
    0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41,
    0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80,
    0x41, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
    0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x0
```

```

0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81,
0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0,
0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01,
0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81,
0x40
};
/*CRC table for calculate with polynom 0xA001 with init value 0xFFFF, Low half word*/
rom unsigned char CRC_Table_Lo[] = {
0x00, 0xC0, 0xC1, 0x01, 0xC3, 0x03, 0x02, 0xC2, 0xC6, 0x06, 0x07, 0xC7, 0x05, 0xC5, 0xC4,
0x04, 0xCC, 0x0C, 0x0D, 0xCD, 0x0F, 0xCF, 0xCE, 0x0E, 0x0A, 0xCA, 0xCB, 0x0B, 0xC9, 0x09,
0x08, 0xC8, 0xD8, 0x18, 0x19, 0xD9, 0x1B, 0xDB, 0xDA, 0x1A, 0x1E, 0xDE, 0xDF, 0x1F, 0xDD,
0x1D, 0x1C, 0xDC, 0x14, 0xD4, 0xD5, 0x15, 0xD7, 0x17, 0x16, 0xD6, 0xD2, 0x12, 0x13, 0xD3,
0x11, 0xD1, 0xD0, 0x10, 0xF0, 0x30, 0x31, 0xF1, 0x33, 0xF3, 0xF2, 0x32, 0x36, 0xF6, 0xF7,
0x37, 0xF5, 0x35, 0x34, 0xF4, 0x3C, 0xFC, 0xFD, 0x3D, 0xFF, 0x3F, 0x3E, 0xFE, 0xFA, 0x3A,
0x3B, 0xFB, 0x39, 0xF9, 0xF8, 0x38, 0x28, 0xE8, 0xE9, 0x29, 0xEB, 0x2B, 0x2A, 0xEA, 0xEE,
0x2E, 0x2F, 0xEF, 0x2D, 0xED, 0xEC, 0x2C, 0xE4, 0x24, 0x25, 0xE5, 0x27, 0xE7, 0xE6, 0x26,
0x22, 0xE2, 0xE3, 0x23, 0xE1, 0x21, 0x20, 0xE0, 0xA0, 0x60, 0x61, 0xA1, 0x63, 0xA3, 0xA2,
0x62, 0x66, 0xA6, 0xA7, 0x67, 0xA5, 0x65, 0x64, 0xA4, 0x6C, 0xAC, 0xAD, 0x6D, 0xAF, 0x6F,
0x6E, 0xAE, 0xAA, 0x6A, 0x6B, 0xAB, 0x69, 0xA9, 0xA8, 0x68, 0x78, 0xB8, 0xB9, 0x79, 0xBB,
0x7B, 0x7A, 0xBA, 0xBE, 0x7E, 0x7F, 0xBF, 0x7D, 0xBD, 0xBC, 0x7C, 0xB4, 0x74, 0x75, 0xB5,
0x77, 0xB7, 0xB6, 0x76, 0x72, 0xB2, 0xB3, 0x73, 0xB1, 0x71, 0x70, 0xB0, 0x50, 0x90, 0x91,
0x51, 0x93, 0x53, 0x52, 0x92, 0x96, 0x56, 0x57, 0x97, 0x55, 0x95, 0x94, 0x54, 0x9C, 0x5C,
0x5D, 0x9D, 0x5F, 0x9F, 0x9E, 0x5E, 0x5A, 0x9A, 0x9B, 0x5B, 0x99, 0x59, 0x58, 0x98, 0x88,
0x48, 0x49, 0x89, 0x4B, 0x8B, 0x8A, 0x4A, 0x4E, 0x8E, 0x8F, 0x4F, 0x8D, 0x4D, 0x4C, 0x8C,
0x44, 0x84, 0x85, 0x45, 0x87, 0x47, 0x46, 0x86, 0x82, 0x42, 0x43, 0x83, 0x41, 0x81, 0x80,
0x40
};

unsigned short ModBus_CRC16( unsigned char * Buffer, unsigned short Length )
{
    unsigned char CRCHi = 0xFF;
    unsigned char CRCLo = 0xFF;
    int Index;
    unsigned short ret;

    while( Length-- )
    {
        Index = CRCLo ^ *Buffer++;
        CRCLo = CRCHi ^ CRC_Table_Hi[Index];
        CRCHi = CRC_Table_Lo[Index];
    }
    ret=((unsigned short)CRCHi << 8);
    ret|=(unsigned short)CRCLo;
    return ret;
}

```

CRC generation functions - Without Table

```

unsigned short ModBus_CRC16( unsigned char * Buffer, unsigned short Length )
{
    /* ModBus_CRC16 Calculatd CRC16 with polynome 0xA001 and init value 0xFFFF
    Input *Buffer - pointer on data
    Input Length - number byte in buffer
    Output - calculated CRC16
    */
    unsigned int cur_crc;

    cur_crc=0xFFFF;
    do
    {
        unsigned int i = 8;
        cur_crc = cur_crc ^ *Buffer++;
        do
        {
            if (0x0001 & cur_crc)
            {
                cur_crc >>= 1;
                cur_crc ^= 0xA001;
            }
            else
            {
                cur_crc >>= 1;
            }
        }
        while (--i);
    }
    while (--Length);

    return cur_crc;
}

```

2. READING COMMAND STRUCTURE

The master communication device can send commands to the instrument to read its status, setup and the measured values.

More registers can be read, at the same time, sending a single command, only if the registers are consecutive (see chapter 5). The reading command is structured as follows.

2.1 Modbus RTU

Values contained both in Query or Response messages are in hex format.

Query example in case of MODBUS RTU: 01030002000265CB

Example	Byte	Description	No. of bytes
01	-	Slave address	1
03	-	Function code	1
00	High	Starting register	2
02	Low		
00	High	No. of words to be read	2
02	Low		
65	High	Error check (CRC)	2
CB	Low		

Response example in case of MODBUS RTU: 01030400035571F547

Example	Byte	Description	No. of bytes
01	-	Slave address	1
03	-	Function code	1
04	-	Byte count	1
00	High	Requested data: PHASE 2 TO NEUTRAL VOLTAGE VALUE	4
03	Low		
55	High		
71	Low		
F5	High	Error check (CRC)	2
47	Low		

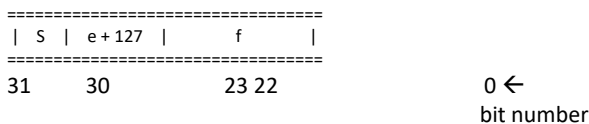
2.2 Floating Point as per IEEE Standard

The basic format allows a IEEE standard floating-point number to be represented in a single 32 bit format, as shown below:

$$N.n = (-1)^S 2^{e'-127} (1.f)$$

where **S** is the sign bit, **e'** is the first part of the exponent and **f** is the decimal fraction placed next to 1. Internally the exponent is 8 bits in length and the stored fraction is 23 bits long. A round to nearest method is applied to the calculated value of floating point.

The floating-point format is shown as follows:



where:

	bit length
Sign	1
Exponent	8
Fraction	23 + (1)
Total	m = 32 + (1)
Exponent	
Min e'	0
Max e'	255
Bias	127

NOTE: Fractions (decimals) are always shown while the leading 1 (hidden bit) is not stored.

Example of conversion of value shown with floating point

Value read with floating point:

45AACC00₍₁₆₎

Value converted in binary format:

0	10001011	010101011001100000000000 ₍₂₎
sign	exponent	fraction

sign = 0

exponent = 10001011₍₂₎ = 139₍₁₀₎

fraction = 010101011001100000000000₍₂₎ / 8388608₍₁₀₎ =
= 2804736₍₁₀₎ / 8388608₍₁₀₎ = 0.334350585₍₁₀₎

$N.n = (-1)^S 2^{e'-127} (1+f) =$
 $= (-1)^0 2^{139-127} (1.334350585) =$
 $= (+1) (4096) (1.334350585) =$
 $= 5465.5$

3. WRITING COMMAND STRUCTURE

The master communication device can send commands to the instrument to program it.

More settings can be carried out, at the same time, sending a single command, only if the relevant registers are consecutive (see chapter 5). The writing command is structured as follows.

3.1 Modbus RTU

Values contained both in Request or Response messages are in hex format.

Writing commands can be sent with different function codes, according to the register number:

Function code \$06 = write a single register

Function code \$10 = write multiple registers

Query example in case of MODBUS RTU with Function code \$06: 0106060000008942

Example	Byte	Description	No. of bytes
01	-	Slave address	1
06	-	Function code	1
06	High	Starting register	2
00	Low		
00	High	Data for programming: ENABLE SETUP	2
00	Low		
89	High	Error check (CRC)	2
42	Low		

Response example in case of MODBUS RTU with Function code \$06: 0106060000008942

Example	Byte	Description	No. of bytes
01	-	Slave address	1
06	-	Function code	1
06	High	Starting register	2
00	Low		
00	High	Programmed data	2
00	Low		
89	High	Error check (CRC)	2
42	Low		

Query example in case of MODBUS RTU with Function code \$10: 0110060000060C0001000100070000000184DA

Example	Byte	Description	No. of bytes
01	-	Slave address	1
10	-	Function code	1
06	High	Starting register	2
00	Low		
00	High	No. of words to be written	2
06	Low		
0C	-	Data byte counter	1
00	High	Data for programming: ENABLE SETUP	2
01	Low		
00	High	Data for programming: SET PASSWORD TO 0001	2
01	Low		
00	High	Data for programming: SET MODBUS ADDRESS TO 0001	2
01	Low		
00	High	Data for programming: SET MODBUS BAUDRATE TO 19200BPS	2
07	Low		
00	High	Data for programming: SET MODBUS PARITY TO NONE	2
00	Low		
00	High	Data for programming: SET MODBUS STOP BIT TO 1	2
01	Low		
84	High	Error check (CRC)	2
DA	Low		

Response example in case of MODBUS RTU with Function code \$10: 0110060000064083

Example	Byte	Description	No. of bytes
01	-	Slave address	1
10	-	Function code	1
06	High	Starting register	2
00	Low		
00	High	No. of written words	2
06	Low		
40	High	Error check (CRC)	2
83	Low		

4. EXCEPTION CODES

When the instrument receives a not-valid query, an error message (exception code) is sent.

The possible exception codes are as follows.

4.1 Modbus RTU

Values contained in Response messages are in hex format.

Response example in case of MODBUS RTU: 01830131F0

Example	Byte	Description	No. of bytes
01	-	Slave address	1
83	-	Function code (80+03)	1
01	-	Exception code	1
31	High	Error check (CRC)	2
F0	Low		

Exception codes for MODBUS RTU are following described:

- \$01** **ILLEGAL FUNCTION:** the function code received in the query is not an allowable action.
- \$02** **ILLEGAL DATA ADDRESS:** the data address received in the query is not an allowable address (i.e. the combination of register and transfer length is invalid).
- \$03** **ILLEGAL DATA VALUE:** a value contained in the query data field is not an allowable value.
- \$04** **ILLEGAL RESPONSE LENGTH:** the request would generate a response with size bigger than that available for MODBUS protocol.

5. GENERAL INFORMATION ON REGISTER TABLES

NOTE: The register values are in hex format (\$).

NOTE: Possibility to read/write a big number of registers with a single command (max 125 registers).

NOTE: For backward compatibility, please consider that register tables are RegSet 0 type.

Table HEADER	Meaning
PARAMETER	Symbol and description of the parameter to be read/written.
+/-	The parameter can take negative value. Sign Bit Mode: If this column is checked, the read register value can have positive or negative sign. Convert a signed register value as shown in the following instructions: The Most Significant Bit (MSB) indicates the sign as follows: 0=positive (+), 1=negative (-). Negative value example: MSB \$8020 = 1000000000100000 = -32 hex bin dec
INTEGER	INTEGER register data. It shows the Address in hex format, the corresponding Word number and the Unit of measure (if any).
IEEE	IEEE standard register data. It shows the Address in hex format, the corresponding Word number and the Unit of measure (if any).
MODEL	Availability of the parameter for the corresponding model: 1ph 45A: 45A 1phase meters 1ph 100A: 100A 1phase meters 3ph 100A: 100A 3phase meters 3ph CT: CT 3phase meters 3ph RGW: Rogowski 3phase meters 3ph ML: Multi-Load 3phase meters If checked with ●, the parameter is available. If not checked, the parameter is not available but the register is readable and it replies \$FFFF. If N/A is shown, the register is not available and it cannot be read.
DATA MEANING	Description of data received by a response of a reading command.
PROGRAMMABLE DATA	Description of data which can be sent for a writing command.

6. READING REGISTERS (FUNCTION CODE \$03)

REALTIME VALUES														
PARAMETER		+/-	INTEGER			IEEE			MODEL					
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
U1N	Phase 1 to Neutral Voltage		2	0000	0.001V	2	1000	V			●	●	●	●
U2N	Phase 2 to Neutral Voltage		2	0002	0.001V	2	1002	V			●	●	●	●
U3N	Phase 3 to Neutral Voltage		2	0004	0.001V	2	1004	V			●	●	●	●
V12	Phase 1 to Phase 2 Voltage		2	0006	0.001V	2	1006	V			●	●	●	●
V23	Phase 2 to Phase 3 Voltage		2	0008	0.001V	2	1008	V			●	●	●	●
V31	Phase 3 to Phase 1 Voltage		2	000A	0.001V	2	100A	V			●	●	●	●
VΣ	Phase to Phase Voltage Average		2	000C	0.001V	2	100C	V	●	●	●	●	●	●
I1	Phase 1 Current		2	000E	0.001A	2	100E	A			●	●	●	●
I2	Phase 2 Current		2	0010	0.001A	2	1010	A			●	●	●	●
I3	Phase 3 Current		2	0012	0.001A	2	1012	A			●	●	●	●
IN	Neutral Current (calculated)		2	0014	0.001A	2	1014	A			●	●	●	●
IΣ	Phase Currents Average (system current)		2	0016	0.001A	2	1016	A	●	●	●	●	●	●
±PF1	Phase 1 Power Factor	●	1	0018	x1000	2	1018	-			●	●	●	●
±PF2	Phase 2 Power Factor	●	1	0019	x1000	2	101A	-			●	●	●	●
±PF3	Phase 3 Power Factor	●	1	001A	x1000	2	101C	-			●	●	●	●
±PFΣ	System Power Factor	●	1	001B	x1000	2	101E	-	●	●	●	●	●	●
±P1	Phase 1 Active Power	●	3	001C	0.001W	2	1020	W			●	●	●	●
±P2	Phase 2 Active Power	●	3	001F	0.001W	2	1022	W			●	●	●	●
±P3	Phase 3 Active Power	●	3	0022	0.001W	2	1024	W			●	●	●	●
±PΣ	System Active Power (sum of phases)	●	3	0025	0.001W	2	1026	W	●	●	●	●	●	●
S1	Phase 1 Apparent Power		3	0028	0.001VA	2	1028	VA			●	●	●	●
S2	Phase 2 Apparent Power		3	002B	0.001VA	2	102A	VA			●	●	●	●
S3	Phase 3 Apparent Power		3	002E	0.001VA	2	102C	VA			●	●	●	●
SΣ	System Apparent Power (sum of phases)		3	0031	0.001VA	2	102E	VA	●	●	●	●	●	●
±Q1	Phase 1 Reactive Power	●	3	0034	0.001var	2	1030	var			●	●	●	●
±Q2	Phase 2 Reactive Power	●	3	0037	0.001var	2	1032	var			●	●	●	●
±Q3	Phase 3 Reactive Power	●	3	003A	0.001var	2	1034	var			●	●	●	●
±QΣ	System Reactive Power (sum of phases)	●	3	003D	0.001var	2	1036	var	●	●	●	●	●	●
F	System Frequency		1	0040	0.001Hz	2	1038	Hz	●	●				
PhSequence	Phase rotation sequence (on voltages) \$00=123-CCW, \$01=132-CW, \$02=not defined		1	0041	-	2	103A	-			●	●	●	●
UΣ	Phase to Neutral Voltage Average		2	0042	0.001V	2	103C	V			●	●	●	●
Σ I1+I2+I3	Phase Currents sum		2	0044	0.001A	2	103E	A			●	●	●	●
U1N THD	Phase 1 to Neutral Voltage THD		2	0046	0.001%	2	1040	%						●
U2N THD	Phase 2 to Neutral Voltage THD		2	0048	0.001%	2	1042	%						●
U3N THD	Phase 3 to Neutral Voltage THD		2	004A	0.001%	2	1044	%						●
-	Reserved		2	004C	-	2	1046	-						
V12 THD	Phase 1 to Phase 2 Voltage THD		2	004E	0.001%	2	1048	%						●
V23 THD	Phase 2 to Phase 3 Voltage THD		2	0050	0.001%	2	104A	%						●
-	Reserved		4	0052	-	4	104C	-						
I1 THD	Phase 1 Current THD		2	0056	0.001%	2	1050	%						●
I2 THD	Phase 2 Current THD (N/A in 3.3.2 wiring)		2	0058	0.001%	2	1052	%						●
I3 THD	Phase 3 Current THD		2	005A	0.001%	2	1054	%						●
-	Reserved		2	005C	-	2	1056	-						
ANG U1N-I1	Phase 1 Voltage-Current Angle		2	005E	0.001°	2	1058	°		●	●	●	●	●
ANG U2N-I2	Phase 2 Voltage-Current Angle		2	0060	0.001°	2	105A	°			●	●	●	●
ANG U3N-I3	Phase 3 Voltage-Current Angle		2	0062	0.001°	2	105C	°			●	●	●	●
AVG ANG ULN-IL	Phase Voltage-Current Angles Average		2	0064	0.001°	2	105E	°			●	●	●	●
U1N min	Phase 1 to Neutral Voltage minimum		2	0066	0.001V	2	1060	V	N/A	N/A	N/A	N/A	N/A	●
U2N min	Phase 2 to Neutral Voltage minimum		2	0068	0.001V	2	1062	V	N/A	N/A	N/A	N/A	N/A	●
U3N min	Phase 3 to Neutral Voltage minimum		2	006A	0.001V	2	1064	V	N/A	N/A	N/A	N/A	N/A	●
U1N MAX	Phase 1 to Neutral Voltage MAX		2	006C	0.001V	2	1066	V	N/A	N/A	N/A	N/A	N/A	●
U2N MAX	Phase 2 to Neutral Voltage MAX		2	006E	0.001V	2	1068	V	N/A	N/A	N/A	N/A	N/A	●
U3N MAX	Phase 3 to Neutral Voltage MAX		2	0070	0.001V	2	106A	V	N/A	N/A	N/A	N/A	N/A	●
V12 min	Phase 1 to Phase 2 Voltage minimum		2	0072	0.001V	2	106C	V	N/A	N/A	N/A	N/A	N/A	●
V23 min	Phase 2 to Phase 3 Voltage minimum		2	0074	0.001V	2	106E	V	N/A	N/A	N/A	N/A	N/A	●
V31 min	Phase 3 to Phase 1 Voltage minimum		2	0076	0.001V	2	1070	V	N/A	N/A	N/A	N/A	N/A	●
V12 MAX	Phase 1 to Phase 2 Voltage MAX		2	0078	0.001V	2	1072	V	N/A	N/A	N/A	N/A	N/A	●
V23 MAX	Phase 2 to Phase 3 Voltage MAX		2	007A	0.001V	2	1074	V	N/A	N/A	N/A	N/A	N/A	●
V31 MAX	Phase 3 to Phase 1 Voltage MAX		2	007C	0.001V	2	1076	V	N/A	N/A	N/A	N/A	N/A	●
I1 min	Phase 1 Current minimum		2	007E	0.001A	2	1078	A	N/A	N/A	N/A	N/A	N/A	●
I2 min	Phase 2 Current minimum		2	0080	0.001A	2	107A	A	N/A	N/A	N/A	N/A	N/A	●
I3 min	Phase 3 Current minimum		2	0082	0.001A	2	107C	A	N/A	N/A	N/A	N/A	N/A	●
I1 MAX	Phase 1 Current MAX		2	0084	0.001A	2	107E	A	N/A	N/A	N/A	N/A	N/A	●
I2 MAX	Phase 2 Current MAX		2	0086	0.001A	2	1080	A	N/A	N/A	N/A	N/A	N/A	●
I3 MAX	Phase 3 Current MAX		2	0088	0.001A	2	1082	A	N/A	N/A	N/A	N/A	N/A	●
±P1 min	Phase 1 Active Power minimum	●	3	008A	0.001W	2	1084	W	N/A	N/A	N/A	N/A	N/A	●
±P2 min	Phase 2 Active Power minimum	●	3	008D	0.001W	2	1086	W	N/A	N/A	N/A	N/A	N/A	●

REALTIME VALUES

PARAMETER		+/-	INTEGER			IEEE			MODEL					
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
±P3 min	Phase 3 Active Power minimum	•	3	0090	0.001W	2	1088	W	N/A	N/A	N/A	N/A	N/A	•
±PΣ min	System Active Power (sum of phases) minimum	•	3	0093	0.001W	2	108A	W	N/A	N/A	N/A	N/A	N/A	•
±P1 MAX	Phase 1 Active Power MAX	•	3	0096	0.001W	2	108C	W	N/A	N/A	N/A	N/A	N/A	•
±P2 MAX	Phase 2 Active Power MAX	•	3	0099	0.001W	2	108E	W	N/A	N/A	N/A	N/A	N/A	•
±P3 MAX	Phase 3 Active Power MAX	•	3	009C	0.001W	2	1090	W	N/A	N/A	N/A	N/A	N/A	•
±PΣ MAX	System Active Power (sum of phases) MAX	•	3	009F	0.001W	2	1092	W	N/A	N/A	N/A	N/A	N/A	•
I1 DMD	Phase 1 Current DMD		2	00A2	0.001A	2	1094	A	•	•	•	•	•	•
I2 DMD	Phase 2 Current DMD		2	00A4	0.001A	2	1096	A			•	•	•	•
I3 DMD	Phase 3 Current DMD		2	00A6	0.001A	2	1098	A			•	•	•	•
IN DMD	Neutral Current DMD		2	00A8	0.001A	2	109A	A			•	•	•	•
+PΣ DMD	Positive System Active Power DMD		3	00AA	0.001W	2	109C	W						•
-PΣ DMD	Negative System Active Power DMD	•	3	00AD	0.001W	2	109E	W						•
±PΣ DMD	System Active Power DMD (calculated on +P and -P)	•	3	00B0	0.001W	2	10A0	W	•	•	•	•	•	•
SΣ DMD	System Apparent Power DMD		3	00B3	0.001VA	2	10A2	VA			•	•	•	•
±QΣ DMD	System Reactive Power DMD (calculated on +Q and -Q)	•	3	00B6	0.001var	2	10A4	var			•	•	•	•
I1 DMD MAX	Phase 1 Current DMD MAX		2	00B9	0.001A	2	10A6	A			•	•	•	•
I2 DMD MAX	Phase 2 Current DMD MAX		2	00BB	0.001A	2	10A8	A			•	•	•	•
I3 DMD MAX	Phase 3 Current DMD MAX		2	00BD	0.001A	2	10AA	A			•	•	•	•
IN DMD MAX	Neutral Current DMD MAX		2	00BF	0.001A	2	10AC	A						•
+PΣ DMD MAX	Positive System Active Power DMD MAX		3	00C1	0.001W	2	10AE	W						•
-PΣ DMD MAX	Negative System Active Power DMD MAX	•	3	00C4	0.001W	2	10B0	W						•
±PΣ DMD MAX	System Active Power DMD MAX	•	3	00C7	0.001W	2	10B2	W	•	•	•	•	•	•
SΣ DMD MAX	System Apparent Power DMD MAX		3	00CA	0.001VA	2	10B4	VA			•	•	•	•
±QΣ DMD MAX	System Reactive Power DMD MAX	•	3	00CD	0.001var	2	10B6	var			•	•	•	•

TOTAL COUNTERS

PARAMETER		+/-	INTEGER			IEEE			MODEL					
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
+Ea1	Phase 1 IMP Active Energy		3	0100	Wh	2	1100	Wh			•	•	•	•
+Ea2	Phase 2 IMP Active Energy		3	0103	Wh	2	1102	Wh			•	•	•	•
+Ea3	Phase 3 IMP Active Energy		3	0106	Wh	2	1104	Wh			•	•	•	•
+EaΣ	System IMP Active Energy		3	0109	Wh	2	1106	Wh	•	•	•	•	•	•
-Ea1	Phase 1 EXP Active Energy		3	010C	Wh	2	1108	Wh			•	•	•	•
-Ea2	Phase 2 EXP Active Energy		3	010F	Wh	2	110A	Wh			•	•	•	•
-Ea3	Phase 3 EXP Active Energy		3	0112	Wh	2	110C	Wh			•	•	•	•
-EaΣ	System EXP Active Energy		3	0115	Wh	2	110E	Wh	•	•	•	•	•	•
-	Reserved		96	0118	-	64	1110	-						
+Er1 (IND+CAP)	Phase 1 IMP Reactive Energy		3	0178	varh	2	1150	varh			•	•	•	•
+Er2 (IND+CAP)	Phase 2 IMP Reactive Energy		3	017B	varh	2	1152	varh			•	•	•	•
+Er3 (IND+CAP)	Phase 3 IMP Reactive Energy		3	017E	varh	2	1154	varh			•	•	•	•
-Er1 (IND+CAP)	Phase 1 EXP Reactive Energy		3	0181	varh	2	1156	varh			•	•	•	•
-Er2 (IND+CAP)	Phase 2 EXP Reactive Energy		3	0184	varh	2	1158	varh			•	•	•	•
-Er3 (IND+CAP)	Phase 3 EXP Reactive Energy		3	0187	varh	2	115A	varh			•	•	•	•
+ErΣ (IND+CAP)	System IMP Reactive Energy		3	018A	varh	2	115C	varh	•	•	•	•	•	•
-ErΣ (IND+CAP)	System EXP Reactive Energy		3	018D	varh	2	115E	varh	•	•	•	•	•	•
EsΣ	System Apparent Energy		3	0190	VAh	2	1160	VAh	•	•	•	•	•	•
MHrCnt	Measure Hour Counter		3	0193	0.1h	2	1162	0.1h			•	•		

PARTIAL AND BALANCE COUNTERS

PARAMETER		+/-	INTEGER			IEEE			MODEL					
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
+EaΣ PAR	System IMP Active Energy PARTIAL		3	0400	Wh	2	1400	Wh	•	•	•	•	•	•
-EaΣ PAR	System EXP Active Energy PARTIAL		3	0403	Wh	2	1402	Wh	•	•	•	•	•	•
-	Reserved		9	0406	-	6	1404	-						
EsΣ PAR	System Apparent Energy PARTIAL		3	040F	VAh	2	140A	VAh	•	•	•	•	•	•
-	Reserved		6	0412	-	4	140C	-						
+ErΣ PAR IMP	System IMP Reactive Energy PARTIAL		3	0418	varh	2	1410	varh	•	•	•	•	•	•
-ErΣ PAR EXP	System EXP Reactive Energy PARTIAL		3	041B	varh	2	1412	varh	•	•	•	•	•	•
±EaΣ BAL	System Active Energy BALANCE (IMP-EXP)	•	3	041E	Wh	2	1414	Wh		•	•	•	•	•
-	Reserved		12	0421	-	8	1416	-						
±ErΣ BAL	System Reactive Energy BALANCE (IMP-EXP)	•	3	042D	varh	2	141E	varh		•	•	•	•	•
±Ea1 BAL	Phase 1 Active Energy BALANCE (IMP-EXP)	•	3	0430	Wh	2	1420	Wh						•
±Ea2 BAL	Phase 2 Active Energy BALANCE (IMP-EXP)	•	3	0433	Wh	2	1422	Wh						•
±Ea3 BAL	Phase 3 Active Energy BALANCE (IMP-EXP)	•	3	0436	Wh	2	1424	Wh						•
±Er1 BAL	Phase 1 Reactive Energy BALANCE (IMP-EXP)	•	3	0439	varh	2	1426	varh						•
±Er2 BAL	Phase 2 Reactive Energy BALANCE (IMP-EXP)	•	3	043C	varh	2	1428	varh						•
±Er3 BAL	Phase 3 Reactive Energy BALANCE (IMP-EXP)	•	3	043F	varh	2	142A	varh						•

TARIFF 1 COUNTERS

PARAMETER		+/-	INTEGER			IEEE			MODEL					
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
-	Reserved		9	0200	-	2	1200	-						N/A
+EaΣ T1	System IMP Active Energy TARIFF1		3	0209	Wh	2	1206	Wh	•	•	•	•	•	N/A
-	Reserved		12	020C	-	8	1208	-						N/A
-EaΣ T1	System EXP Active Energy TARIFF1		3	0215	Wh	2	120E	Wh	•	•	•	•	•	N/A
-	Reserved		96	0218	-	64	1210	-						N/A
+ErΣ T1	System IMP Reactive Energy TARIFF1		3	0278	varh	2	1250	varh	•	•	•	•	•	N/A
-ErΣ T1	System EXP Reactive Energy TARIFF1		3	027B	varh	2	1252	varh	•	•	•	•	•	N/A
-	Reserved		3	027E	-	2	1254	-						N/A
EsΣ T1	System Apparent Energy TARIFF1		3	0281	VAh	2	1256	VAh	•	•	•	•	•	N/A

TARIFF 2 COUNTERS

PARAMETER		+/-	INTEGER			IEEE			MODEL					
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
-	Reserved		9	0300	-	2	1300	-						N/A
+Ea $\sum$ T2	System IMP Active Energy TARIFF2		3	0309	Wh	2	1306	Wh	•	•	•	•	•	N/A
-	Reserved		12	030C	-	8	1308	-						N/A
-Ea $\sum$ T2	System EXP Active Energy TARIFF2		3	0315	Wh	2	130E	Wh	•	•	•	•	•	N/A
-	Reserved		96	0318	-	64	1310	-						N/A
+Er $\sum$ T2	System IMP Reactive Energy TARIFF2		3	0378	varh	2	1350	varh	•	•	•	•	•	N/A
-Er $\sum$ T2	System EXP Reactive Energy TARIFF2		3	037B	varh	2	1352	varh	•	•	•	•	•	N/A
-	Reserved		3	037E	-	2	1354	-						N/A
Es $\sum$ T2	System Apparent Energy TARIFF2		3	0381	VAh	2	1356	VAh	•	•	•	•	•	N/A

TARIFF 3 COUNTERS

PARAMETER		+/-	INTEGER			IEEE			MODEL					
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
-	Reserved		9	2100	-	2	1500	-						N/A
+Ea $\sum$ T3	System IMP Active Energy TARIFF3		3	2109	Wh	2	1506	Wh	•	•	•	•	•	N/A
-	Reserved		12	210C	-	8	1508	-						N/A
-Ea $\sum$ T3	System EXP Active Energy TARIFF3		3	2115	Wh	2	150E	Wh	•	•	•	•	•	N/A
-	Reserved		96	2118	-	64	1510	-						N/A
+Er $\sum$ T3	System IMP Reactive Energy TARIFF3		3	2178	varh	2	1550	varh	•	•	•	•	•	N/A
-Er $\sum$ T3	System EXP Reactive Energy TARIFF3		3	217B	varh	2	1552	varh	•	•	•	•	•	N/A
-	Reserved		3	217E	-	2	1554	-						N/A
Es $\sum$ T3	System Apparent Energy TARIFF3		3	2181	VAh	2	1556	VAh	•	•	•	•	•	N/A

TARIFF 4 COUNTERS

PARAMETER		+/-	INTEGER			IEEE			MODEL					
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
-	Reserved		9	2200	-	2	1600	-						N/A
+Ea $\sum$ T4	System IMP Active Energy TARIFF4		3	2209	Wh	2	1606	Wh	•	•	•	•	•	N/A
-	Reserved		12	220C	-	8	1608	-						N/A
-Ea $\sum$ T4	System EXP Active Energy TARIFF4		3	2215	Wh	2	160E	Wh	•	•	•	•	•	N/A
-	Reserved		96	2218	-	64	1610	-						N/A
+Er $\sum$ T4	System IMP Reactive Energy TARIFF4		3	2278	varh	2	1650	varh	•	•	•	•	•	N/A
-Er $\sum$ T4	System EXP Reactive Energy TARIFF4		3	227B	varh	2	1652	varh	•	•	•	•	•	N/A
-	Reserved		3	227E	-	2	1654	-						N/A
Es $\sum$ T4	System Apparent Energy TARIFF4		3	2281	VAh	2	1656	VAh	•	•	•	•	•	N/A

INSTRUMENT INFO

PARAMETER		INTEGER		DATA MEANING	MODEL					
Symbol	Description	Words	Address	Values	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
SN-LOT	Serial number and Lot number	5	0500	The value is structured as follows: word1(MSw)-2=Serial number (SN) word3-4=Lot number (LOT, fixed value) word5(LSw)=not used Convert the read Hex value in Dec value. e.g. \$0E4E1BFF0007A1200000: SN=\$0E4E1BFF => 239999999 LOT=\$0007A120 => 500000 (fixed value)	•	•	•	•	•	•
MODEL	Model	1	0505	\$20=1ph 45A \$21=1ph 100A \$22=3ph 100A \$23=3ph 4ML \$25=3ph RGW \$26=3ph CT \$27=3ph 2ML	•	•	•	•	•	•
TYPE	Type	1	0506	\$02=1phase meter \$09=3phase meter	•	•	•	•	•	•
FW REL	Firmware release	1	0507	Major & minor version of Firmware release. Convert the read Hex value in Dec value. e.g. \$0D80=3456 major FW=34, minor FW=56 => 12.34.56 FW on display	•	•	•	•	•	•
HW VER	Hardware version	1	0508	e.g. \$0101 => ver. 1.01	•	•	•	•	•	•
OEM	OEM code	2	0509	Fixed to \$414C474F	•	•	•	•	•	•
TAR	Tariff in use	1	050B	\$01=Tariff 1 \$02=Tariff 2 \$03=Tariff 3 \$04=Tariff 4	•	•	•	•	•	
PRI/SEC	Primary/Secondary value in use	1	050C	\$00=Primary (fixed for direct meters) \$01=Secondary (for 3phCT only)	•	•	•	•	•	•
ERR	Error code	1	050D	\$00=No error \$01=Memory error \$02=Calibration parameter error \$03=Metrology parameter error	•	•	•	•	•	•
-	Reserved	15	050E	-						
SIGN REPR	Sign representation method	1	051D	\$00=Sign bit	•	•	•	•	•	•
-	Reserved	6	051E	-						
CHK	Checksum	2	0524	8 Hex characters	•	•	•	•	•	•
ADDR	Modbus Address	1	0602	\$01...\$F7	•	•	•	•	•	•
BAUD	Modbus Baudrate	1	0603	\$04=2400 bps \$05=4800 bps \$06=9600 bps \$07=19200 bps \$08=38400 bps	•	•	•	•	•	•
PARITY	Modbus Parity	1	0604	\$00=None \$01=Odd \$02=Even	•	•	•	•	•	•
STOP BIT	Modbus Stop bit	1	0605	\$01=1 \$02=2	•	•	•	•	•	•
CT PRI (CH1)	CT or Rogowski coil primary	1	0606	Convert the read Hex value in Dec value. e.g. \$270F=9999A				•	•	•
CT SEC	CT or Rogowski coil secondary	1	0607	\$00=1A \$01=5A \$09=100mV (fixed for 3phML & 3phRGW only)	•	•	•	•	•	•
PT PRI	PT primary	1	0608	Convert the read Hex value in Dec value. e.g. \$270F=9999V				•	•	
PT SEC	PT secondary	1	0609	Convert the read Hex value in Dec value. e.g. \$01F4=500V				•	•	
I DIR	Current direction	1	060A	Bit field coding: bit15(MSb)...bit12 not used, always 0 bit11=I3 CH4 for 3phML only bit10=I2 CH4 for 3phML only bit9=I1 CH4 for 3phML only bit8=I3 CH3 for 3phML only bit7=I2 CH3 for 3phML only bit6=I1 CH3 for 3phML only bit5=I3 CH2 for 3phML only bit4=I2 CH2 for 3phML only bit3=I1 CH2 for 3phML only bit2=I3 bit1=I2 bit0(LSb)=I1 Bit=0 means Forward current (FWD) Bit=1 means Reverse current (REV)				•	•	•

INSTRUMENT INFO										
PARAMETER		INTEGER		DATA MEANING	MODEL					
Symbol	Description	Words	Address	Values	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
WIR	Wiring mode	1	060B	\$01=3phases, 4 wires, 3 currents \$02=3phases, 3 wires, 2 currents \$03=1phase, 2 wires, 1 current \$04=3phases, 3 wires, 3 currents	●	●	●	●	●	●
DMD INT	Demand integration time	1	060C	\$05=5 min \$08=8 min \$0A=10 min \$0F=15 min \$14=20 min \$1E=30 min \$3C=60 min	●	●	●	●	●	●
S01-EN	Pulse output 1 energy type	1	060D	\$01=+EaΣ \$02=-EaΣ \$03=+ErΣ \$04=-ErΣ	●	●	●	●	●	
S01-RATE	Pulse output 1 rate	1	060E	\$01=0.001 En/imp \$02=0.01 En/imp \$03=0.1 En/imp \$04=1 En/imp \$05=10 En/imp \$06=100 En/imp \$07=1000 En/imp	●	●	●	●	●	
S01-DUR	Pulse output 1 duration	1	060F	\$3C=60 s \$64=100 s \$C8=200 s	●	●	●	●	●	
BCKLIT	Backlight time	1	0610	\$00=always ON \$01=1 min \$05=5 min \$0A=10 min \$1E=30 min \$3C=60 min \$78=120 min		●	●	●	●	●
MHrCntThr	Measure Hour counter threshold	1	0613	Threshold value conversion changes according to the model. For 100A model: Convert the read Hex value in Dec value and multiply it by 0.01 e.g.: \$1388 = 5000 => 50 For CT model: Convert the read Hex value in Dec value and multiply it by one of the following value according to the CT primary: 0.005 if CT primary is <10 0.01 if CT primary is <100 0.1 if CT primary is <1000 1 if CT primary is <10000 e.g. if CT primary is <10: \$0258 = 600 => 3			●	●		
CT PRI (CH2)	CT primary of channel 2	1	0614	\$00A0=160A \$00FA=250A \$0276=630A \$03E8=1000A	N/A	N/A	N/A	N/A	N/A	●
CT PRI (CH3)	CT primary of channel 3	1	0615	\$00A0=160A \$00FA=250A \$0276=630A \$03E8=1000A	N/A	N/A	N/A	N/A	N/A	●
CT PRI (CH4)	CT primary of channel 4	1	0616	\$00A0=160A \$00FA=250A \$0276=630A \$03E8=1000A	N/A	N/A	N/A	N/A	N/A	●

6.1 READING registers for channel 2-3-4 (only Multi-Load meters)

Refer to the following table for parameters of the other channels (CH2, CH3, CH4):

REALTIME VALUES - CHANNEL 2															
PARAMETER		+/-	INTEGER			IEEE			MODEL						
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML	
U1N – CH2	Phase 1 to Neutral Voltage – Channel 2		2	3000	0.001V	2	4000	V	N/A	N/A	N/A	N/A	N/A	•	
U2N – CH2	Phase 2 to Neutral Voltage – Channel 2		2	3002	0.001V	2	4002	V	N/A	N/A	N/A	N/A	N/A	•	
U3N – CH2	Phase 3 to Neutral Voltage – Channel 2		2	3004	0.001V	2	4004	V	N/A	N/A	N/A	N/A	N/A	•	
V12 – CH2	Phase 1 to Phase 2 Voltage – Channel 2		2	3006	0.001V	2	4006	V	N/A	N/A	N/A	N/A	N/A	•	
V23 – CH2	Phase 2 to Phase 3 Voltage – Channel 2		2	3008	0.001V	2	4008	V	N/A	N/A	N/A	N/A	N/A	•	
V31 – CH2	Phase 3 to Phase 1 Voltage – Channel 2		2	300A	0.001V	2	400A	V	N/A	N/A	N/A	N/A	N/A	•	
VΣ – CH2	Phase to Phase Voltage Average – Channel 2		2	300C	0.001V	2	400C	V	N/A	N/A	N/A	N/A	N/A	•	
I1 – CH2	Phase 1 Current – Channel 2		2	300E	0.001A	2	400E	A	N/A	N/A	N/A	N/A	N/A	•	
I2 – CH2	Phase 2 Current – Channel 2		2	3010	0.001A	2	4010	A	N/A	N/A	N/A	N/A	N/A	•	
I3 – CH2	Phase 3 Current – Channel 2		2	3012	0.001A	2	4012	A	N/A	N/A	N/A	N/A	N/A	•	
IN – CH2	Neutral Current (calculated) – Channel 2		2	3014	0.001A	2	4014	A	N/A	N/A	N/A	N/A	N/A	•	
IΣ – CH2	Phase Currents Average (system current) – Channel 2		2	3016	0.001A	2	4016	A	N/A	N/A	N/A	N/A	N/A	•	
±PF1 – CH2	Phase 1 Power Factor – Channel 2	•	1	3018	x1000	2	4018	-	N/A	N/A	N/A	N/A	N/A	•	
±PF2 – CH2	Phase 2 Power Factor – Channel 2	•	1	3019	x1000	2	401A	-	N/A	N/A	N/A	N/A	N/A	•	
±PF3 – CH2	Phase 3 Power Factor – Channel 2	•	1	301A	x1000	2	401C	-	N/A	N/A	N/A	N/A	N/A	•	
±PFΣ – CH2	System Power Factor – Channel 2	•	1	301B	x1000	2	401E	-	N/A	N/A	N/A	N/A	N/A	•	
±P1 – CH2	Phase 1 Active Power – Channel 2	•	3	301C	0.001W	2	4020	W	N/A	N/A	N/A	N/A	N/A	•	
±P2 – CH2	Phase 2 Active Power – Channel 2	•	3	301F	0.001W	2	4022	W	N/A	N/A	N/A	N/A	N/A	•	
±P3 – CH2	Phase 3 Active Power – Channel 2	•	3	3022	0.001W	2	4024	W	N/A	N/A	N/A	N/A	N/A	•	
±PΣ – CH2	System Active Power (sum of phases) – Channel 2	•	3	3025	0.001W	2	4026	W	N/A	N/A	N/A	N/A	N/A	•	
S1 – CH2	Phase 1 Apparent Power – Channel 2		3	3028	0.001VA	2	4028	VA	N/A	N/A	N/A	N/A	N/A	•	
S2 – CH2	Phase 2 Apparent Power – Channel 2		3	302B	0.001VA	2	402A	VA	N/A	N/A	N/A	N/A	N/A	•	
S3 – CH2	Phase 3 Apparent Power – Channel 2		3	302E	0.001VA	2	402C	VA	N/A	N/A	N/A	N/A	N/A	•	
SΣ – CH2	System Apparent Power (sum of phases) – Channel 2		3	3031	0.001VA	2	402E	VA	N/A	N/A	N/A	N/A	N/A	•	
±Q1 – CH2	Phase 1 Reactive Power – Channel 2	•	3	3034	0.001var	2	4030	var	N/A	N/A	N/A	N/A	N/A	•	
±Q2 – CH2	Phase 2 Reactive Power – Channel 2	•	3	3037	0.001var	2	4032	var	N/A	N/A	N/A	N/A	N/A	•	
±Q3 – CH2	Phase 3 Reactive Power – Channel 2	•	3	303A	0.001var	2	4034	var	N/A	N/A	N/A	N/A	N/A	•	
±QΣ – CH2	System Reactive Power (sum of phases) – Channel 2	•	3	303D	0.001var	2	4036	var	N/A	N/A	N/A	N/A	N/A	•	
F – CH2	System Frequency – Channel 2		1	3040	0.001Hz	2	4038	Hz	N/A	N/A	N/A	N/A	N/A	•	
PhSequence – CH2	Phase rotation sequence (on voltages) – Channel 2 \$00=123-CCW, \$01=132-CW, \$02=not defined		1	3041	-	2	403A	-	N/A	N/A	N/A	N/A	N/A	•	
UΣ – CH2	Phase to Neutral Voltage Average – Channel 2		2	3042	0.001V	2	403C	V	N/A	N/A	N/A	N/A	N/A	•	
Σ I1+I2+I3 – CH2	Phase Currents sum – Channel 2		2	3044	0.001A	2	403E	A	N/A	N/A	N/A	N/A	N/A	•	
U1N THD – CH2	Phase 1 to Neutral Voltage THD – Channel 2		2	3046	0.001%	2	4040	%	N/A	N/A	N/A	N/A	N/A	•	
U2N THD – CH2	Phase 2 to Neutral Voltage THD – Channel 2		2	3048	0.001%	2	4042	%	N/A	N/A	N/A	N/A	N/A	•	
U3N THD – CH2	Phase 3 to Neutral Voltage THD – Channel 2		2	304A	0.001%	2	4044	%	N/A	N/A	N/A	N/A	N/A	•	
-	Reserved		2	304C	-	2	4046	-							
V12 THD – CH2	Phase 1 to Phase 2 Voltage THD – Channel 2		2	304E	0.001%	2	4048	%	N/A	N/A	N/A	N/A	N/A	•	
V23 THD – CH2	Phase 2 to Phase 3 Voltage THD – Channel 2		2	3050	0.001%	2	404A	%	N/A	N/A	N/A	N/A	N/A	•	
-	Reserved		4	3052	-	4	404C	-							
I1 THD – CH2	Phase 1 Current THD – Channel 2		2	3056	0.001%	2	4050	%	N/A	N/A	N/A	N/A	N/A	•	
I2 THD – CH2	Phase 2 Current THD (N/A in 3.3.2 wiring) – Channel 2		2	3058	0.001%	2	4052	%	N/A	N/A	N/A	N/A	N/A	•	
I3 THD – CH2	Phase 3 Current THD – Channel 2		2	305A	0.001%	2	4054	%	N/A	N/A	N/A	N/A	N/A	•	

REALTIME VALUES - CHANNEL 3

PARAMETER		+/ -	INTEGER			IEEE			MODEL					
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
U1N – CH3	Phase 1 to Neutral Voltage – Channel 3		2	5000	0.001V	2	6000	V	N/A	N/A	N/A	N/A	N/A	●
U2N – CH3	Phase 2 to Neutral Voltage – Channel 3		2	5002	0.001V	2	6002	V	N/A	N/A	N/A	N/A	N/A	●
U3N – CH3	Phase 3 to Neutral Voltage – Channel 3		2	5004	0.001V	2	6004	V	N/A	N/A	N/A	N/A	N/A	●
V12 – CH3	Phase 1 to Phase 2 Voltage – Channel 3		2	5006	0.001V	2	6006	V	N/A	N/A	N/A	N/A	N/A	●
V23 – CH3	Phase 2 to Phase 3 Voltage – Channel 3		2	5008	0.001V	2	6008	V	N/A	N/A	N/A	N/A	N/A	●
V31 – CH3	Phase 3 to Phase 1 Voltage – Channel 3		2	500A	0.001V	2	600A	V	N/A	N/A	N/A	N/A	N/A	●
VΣ – CH3	Phase to Phase Voltage Average – Channel 3		2	500C	0.001V	2	600C	V	N/A	N/A	N/A	N/A	N/A	●
I1 – CH3	Phase 1 Current – Channel 3		2	500E	0.001A	2	600E	A	N/A	N/A	N/A	N/A	N/A	●
I2 – CH3	Phase 2 Current – Channel 3		2	5010	0.001A	2	6010	A	N/A	N/A	N/A	N/A	N/A	●
I3 – CH3	Phase 3 Current – Channel 3		2	5012	0.001A	2	6012	A	N/A	N/A	N/A	N/A	N/A	●
IN – CH3	Neutral Current (calculated) – Channel 3		2	5014	0.001A	2	6014	A	N/A	N/A	N/A	N/A	N/A	●
IΣ – CH3	Phase Currents Average (system current) – Channel 3		2	5016	0.001A	2	6016	A	N/A	N/A	N/A	N/A	N/A	●
±PF1 – CH3	Phase 1 Power Factor – Channel 3	●	1	5018	x1000	2	6018	-	N/A	N/A	N/A	N/A	N/A	●
±PF2 – CH3	Phase 2 Power Factor – Channel 3	●	1	5019	x1000	2	601A	-	N/A	N/A	N/A	N/A	N/A	●
±PF3 – CH3	Phase 3 Power Factor – Channel 3	●	1	501A	x1000	2	601C	-	N/A	N/A	N/A	N/A	N/A	●
±PFS – CH3	System Power Factor – Channel 3	●	1	501B	x1000	2	601E	-	N/A	N/A	N/A	N/A	N/A	●
±P1 – CH3	Phase 1 Active Power – Channel 3	●	3	501C	0.001W	2	6020	W	N/A	N/A	N/A	N/A	N/A	●
±P2 – CH3	Phase 2 Active Power – Channel 3	●	3	501F	0.001W	2	6022	W	N/A	N/A	N/A	N/A	N/A	●
±P3 – CH3	Phase 3 Active Power – Channel 3	●	3	5022	0.001W	2	6024	W	N/A	N/A	N/A	N/A	N/A	●
±PS – CH3	System Active Power (sum of phases) – Channel 3	●	3	5025	0.001W	2	6026	W	N/A	N/A	N/A	N/A	N/A	●
S1 – CH3	Phase 1 Apparent Power – Channel 3		3	5028	0.001VA	2	6028	VA	N/A	N/A	N/A	N/A	N/A	●
S2 – CH3	Phase 2 Apparent Power – Channel 3		3	502B	0.001VA	2	602A	VA	N/A	N/A	N/A	N/A	N/A	●
S3 – CH3	Phase 3 Apparent Power – Channel 3		3	502E	0.001VA	2	602C	VA	N/A	N/A	N/A	N/A	N/A	●
SΣ – CH3	System Apparent Power (sum of phases) – Channel 3		3	5031	0.001VA	2	602E	VA	N/A	N/A	N/A	N/A	N/A	●
±Q1 – CH3	Phase 1 Reactive Power – Channel 3	●	3	5034	0.001var	2	6030	var	N/A	N/A	N/A	N/A	N/A	●
±Q2 – CH3	Phase 2 Reactive Power – Channel 3	●	3	5037	0.001var	2	6032	var	N/A	N/A	N/A	N/A	N/A	●
±Q3 – CH3	Phase 3 Reactive Power – Channel 3	●	3	503A	0.001var	2	6034	var	N/A	N/A	N/A	N/A	N/A	●
±QS – CH3	System Reactive Power (sum of phases) – Channel 3	●	3	503D	0.001var	2	6036	var	N/A	N/A	N/A	N/A	N/A	●
F – CH3	System Frequency – Channel 3		1	5040	0.001Hz	2	6038	Hz	N/A	N/A	N/A	N/A	N/A	●
PhSequence – CH3	Phase rotation sequence (on voltages) – Channel 3 \$00=123-CCW, \$01=132-CW, \$02=not defined		1	5041	-	2	603A	-	N/A	N/A	N/A	N/A	N/A	●
UΣ – CH3	Phase to Neutral Voltage Average – Channel 3		2	5042	0.001V	2	603C	V	N/A	N/A	N/A	N/A	N/A	●
Σ I1+I2+I3 – CH3	Phase Currents sum – Channel 3		2	5044	0.001A	2	603E	A	N/A	N/A	N/A	N/A	N/A	●
U1N THD – CH3	Phase 1 to Neutral Voltage THD – Channel 3		2	5046	0.001%	2	6040	%	N/A	N/A	N/A	N/A	N/A	●
U2N THD – CH3	Phase 2 to Neutral Voltage THD – Channel 3		2	5048	0.001%	2	6042	%	N/A	N/A	N/A	N/A	N/A	●
U3N THD – CH3	Phase 3 to Neutral Voltage THD – Channel 3		2	504A	0.001%	2	6044	%	N/A	N/A	N/A	N/A	N/A	●
-	Reserved		2	504C	-	2	6046	-						
V12 THD – CH3	Phase 1 to Phase 2 Voltage THD – Channel 3		2	504E	0.001%	2	6048	%	N/A	N/A	N/A	N/A	N/A	●
V23 THD – CH3	Phase 2 to Phase 3 Voltage THD – Channel 3		2	5050	0.001%	2	604A	%	N/A	N/A	N/A	N/A	N/A	●
-	Reserved		4	5052	-	4	604C	-						
I1 THD – CH3	Phase 1 Current THD – Channel 3		2	5056	0.001%	2	6050	%	N/A	N/A	N/A	N/A	N/A	●
I2 THD – CH3	Phase 2 Current THD (N/A in 3.3.2 wiring) – Channel 3		2	5058	0.001%	2	6052	%	N/A	N/A	N/A	N/A	N/A	●
I3 THD – CH3	Phase 3 Current THD – Channel 3		2	505A	0.001%	2	6054	%	N/A	N/A	N/A	N/A	N/A	●

REALTIME VALUES - CHANNEL 4

PARAMETER		+/ -	INTEGER			IEEE			MODEL					
Symbol	Description	Signed	Words	Address	Unit of measure	Words	Address	Unit of measure	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
U1N – CH4	Phase 1 to Neutral Voltage – Channel 4		2	7000	0.001V	2	8000	V	N/A	N/A	N/A	N/A	N/A	•
U2N – CH4	Phase 2 to Neutral Voltage – Channel 4		2	7002	0.001V	2	8002	V	N/A	N/A	N/A	N/A	N/A	•
U3N – CH4	Phase 3 to Neutral Voltage – Channel 4		2	7004	0.001V	2	8004	V	N/A	N/A	N/A	N/A	N/A	•
V12 – CH4	Phase 1 to Phase 2 Voltage – Channel 4		2	7006	0.001V	2	8006	V	N/A	N/A	N/A	N/A	N/A	•
V23 – CH4	Phase 2 to Phase 3 Voltage – Channel 4		2	7008	0.001V	2	8008	V	N/A	N/A	N/A	N/A	N/A	•
V31 – CH4	Phase 3 to Phase 1 Voltage – Channel 4		2	700A	0.001V	2	800A	V	N/A	N/A	N/A	N/A	N/A	•
VΣ – CH4	Phase to Phase Voltage Average – Channel 4		2	700C	0.001V	2	800C	V	N/A	N/A	N/A	N/A	N/A	•
I1 – CH4	Phase 1 Current – Channel 4		2	700E	0.001A	2	800E	A	N/A	N/A	N/A	N/A	N/A	•
I2 – CH4	Phase 2 Current – Channel 4		2	7010	0.001A	2	8010	A	N/A	N/A	N/A	N/A	N/A	•
I3 – CH4	Phase 3 Current – Channel 4		2	7012	0.001A	2	8012	A	N/A	N/A	N/A	N/A	N/A	•
IN – CH4	Neutral Current (calculated) – Channel 4		2	7014	0.001A	2	8014	A	N/A	N/A	N/A	N/A	N/A	•
IΣ – CH4	Phase Currents Average (system current) – Channel 4		2	7016	0.001A	2	8016	A	N/A	N/A	N/A	N/A	N/A	•
±PF1 – CH4	Phase 1 Power Factor – Channel 4	•	1	7018	x1000	2	8018	-	N/A	N/A	N/A	N/A	N/A	•
±PF2 – CH4	Phase 2 Power Factor – Channel 4	•	1	7019	x1000	2	801A	-	N/A	N/A	N/A	N/A	N/A	•
±PF3 – CH4	Phase 3 Power Factor – Channel 4	•	1	701A	x1000	2	801C	-	N/A	N/A	N/A	N/A	N/A	•
±PFS – CH4	System Power Factor – Channel 4	•	1	701B	x1000	2	801E	-	N/A	N/A	N/A	N/A	N/A	•
±P1 – CH4	Phase 1 Active Power – Channel 4	•	3	701C	0.001W	2	8020	W	N/A	N/A	N/A	N/A	N/A	•
±P2 – CH4	Phase 2 Active Power – Channel 4	•	3	701F	0.001W	2	8022	W	N/A	N/A	N/A	N/A	N/A	•
±P3 – CH4	Phase 3 Active Power – Channel 4	•	3	7022	0.001W	2	8024	W	N/A	N/A	N/A	N/A	N/A	•
±PS – CH4	System Active Power (sum of phases) – Channel 4	•	3	7025	0.001W	2	8026	W	N/A	N/A	N/A	N/A	N/A	•
S1 – CH4	Phase 1 Apparent Power – Channel 4		3	7028	0.001VA	2	8028	VA	N/A	N/A	N/A	N/A	N/A	•
S2 – CH4	Phase 2 Apparent Power – Channel 4		3	702B	0.001VA	2	802A	VA	N/A	N/A	N/A	N/A	N/A	•
S3 – CH4	Phase 3 Apparent Power – Channel 4		3	702E	0.001VA	2	802C	VA	N/A	N/A	N/A	N/A	N/A	•
SΣ – CH4	System Apparent Power (sum of phases) – Channel 4		3	7031	0.001VA	2	802E	VA	N/A	N/A	N/A	N/A	N/A	•
±Q1 – CH4	Phase 1 Reactive Power – Channel 4	•	3	7034	0.001var	2	8030	var	N/A	N/A	N/A	N/A	N/A	•
±Q2 – CH4	Phase 2 Reactive Power – Channel 4	•	3	7037	0.001var	2	8032	var	N/A	N/A	N/A	N/A	N/A	•
±Q3 – CH4	Phase 3 Reactive Power – Channel 4	•	3	703A	0.001var	2	8034	var	N/A	N/A	N/A	N/A	N/A	•
±QS – CH4	System Reactive Power (sum of phases) – Channel 4	•	3	703D	0.001var	2	8036	var	N/A	N/A	N/A	N/A	N/A	•
F – CH4	System Frequency – Channel 4		1	7040	0.001Hz	2	8038	Hz	N/A	N/A	N/A	N/A	N/A	•
PhSequence – CH4	Phase rotation sequence (on voltages) – Channel 4 \$00=123-CCW, \$01=132-CW, \$02=not defined		1	7041	-	2	803A	-	N/A	N/A	N/A	N/A	N/A	•
UΣ – CH4	Phase to Neutral Voltage Average – Channel 4		2	7042	0.001V	2	803C	V	N/A	N/A	N/A	N/A	N/A	•
Σ I1+I2+I3 – CH4	Phase Currents sum – Channel 4		2	7044	0.001A	2	803E	A	N/A	N/A	N/A	N/A	N/A	•
U1N THD – CH4	Phase 1 to Neutral Voltage THD – Channel 4		2	7046	0.001%	2	8040	%	N/A	N/A	N/A	N/A	N/A	•
U2N THD – CH4	Phase 2 to Neutral Voltage THD – Channel 4		2	7048	0.001%	2	8042	%	N/A	N/A	N/A	N/A	N/A	•
U3N THD – CH4	Phase 3 to Neutral Voltage THD – Channel 4		2	704A	0.001%	2	8044	%	N/A	N/A	N/A	N/A	N/A	•
-	Reserved		2	704C	-	2	8046	-						
V12 THD – CH4	Phase 1 to Phase 2 Voltage THD – Channel 4		2	704E	0.001%	2	8048	%	N/A	N/A	N/A	N/A	N/A	•
V23 THD – CH4	Phase 2 to Phase 3 Voltage THD – Channel 4		2	7050	0.001%	2	804A	%	N/A	N/A	N/A	N/A	N/A	•
-	Reserved		4	7052	-	4	804C	-						
I1 THD – CH4	Phase 1 Current THD – Channel 4		2	7056	0.001%	2	8050	%	N/A	N/A	N/A	N/A	N/A	•
I2 THD – CH4	Phase 2 Current THD (N/A in 3.3.2 wiring) – Channel 4		2	7058	0.001%	2	8052	%	N/A	N/A	N/A	N/A	N/A	•
I3 THD – CH4	Phase 3 Current THD – Channel 4		2	705A	0.001%	2	8054	%	N/A	N/A	N/A	N/A	N/A	•

7. WRITING REGISTERS (FUNCTION CODES \$06, \$10)

To set the instrument, first enable the setup by sending the password (writing register \$0600), then change settings by sending the desired writing command within 60 s.

If register \$0600 is not sent or is sent incorrectly, the next writing command has no effect and the instrument will reply with an exception code.

INSTRUMENT SETUP										
PARAMETER		INTEGER		PROGRAMMABLE DATA	MODEL					
Symbol	Description	Words	Address	Values	1ph 45A	1ph 100A	3ph 100A	3ph CT	3ph RGW	3ph ML
SETUP EN	Enable setup by entering password	1	0600	Send setup password (default \$03E8) to enable the writing command sending. After that, it is allowed to send writing commands within 60 s.		•	•	•	•	•
SETUP PW CHANGE	Setup password change	1	0601	\$0000 ... \$270F		•	•	•	•	•
ADDR	Modbus Address	1	0602	\$01 ... \$F7	•	•	•	•	•	•
BAUD	Modbus Baudrate	1	0603	\$04=2400 bps \$05=4800 bps \$06=9600 bps \$07=19200 bps \$08=38400 bps	•	•	•	•	•	•
PARITY	Modbus Parity	1	0604	\$00=None \$01=Odd \$02=Even	•	•	•	•	•	•
STOP BIT	Modbus Stop bit	1	0605	\$01=1 \$02=2	•	•	•	•	•	•
DMD INT	Demand integration time	1	060C	\$05=5 min \$08=8 min \$0A=10 min \$0F=15 min \$14=20 min \$1E=30 min \$3C=60 min	•	•	•	•	•	•
S01-EN	Pulse output 1 energy type	1	060D	\$01=+EaΣ \$02=-EaΣ \$03=+ErΣ \$04=-ErΣ	•	•	•	•	•	
S01-RATE	Pulse output 1 rate	1	060E	\$01= 0.001 En/imp \$02=0.01 En/imp \$03=0.1 En/imp \$04=1 En/imp \$05=10 En/imp \$06=100 En/imp \$07=1000 En/imp	•	•	•	•	•	
S01-DUR	Pulse output 1 duration	1	060F	\$3C=60 s \$64=100 s \$C8=200 s	•	•	•	•	•	
BCKLIT	Backlit time	1	0610	\$00=Always ON \$01=1 min \$05=5 min \$0A=10 min \$1E=30 min \$3C=60 min \$78=120 min		•	•	•	•	•
RES MD/mM	Reset MAX DMD and MIN/MAX values	1	0611	\$01=Reset all MAX DMD values \$02=Reset all MIN/MAX values (for 3phML only)	•	•	•	•	•	•
RES PAR	Reset Partial counter values	1	0612	\$01=Reset all partial counters	•	•	•	•	•	•
MHrCntThr	Measure Hour counter threshold	1	0613	It must be set considering that threshold range values change according to the model. For 100A model: Ist ... Imax (0.04 ... 99.99) To set threshold, first divide the Dec value to be programmed by 0.01, then convert it in Hex value. e.g.: 50 = 5000 => \$1388 For CT model: Ist*CT ratio ... CT primary To set threshold, first divide the Dec value to be programmed by one of the following value according to the CT primary: 0.005 if CT primary is <10 0.01 if CT primary is <100 0.1 if CT primary is <1000 1 if CT primary is <10000 Then convert it in Hex value. e.g. if CT primary is <10: 3 = 600 => \$0258			•	•		
TAR SWITCH	Tariff switch (available if Tariff Control is set on COM port)	1	0617	\$01=Tariff 1 \$02=Tariff 2 \$03=Tariff 3 \$04=Tariff 4	•	•	•	•	•	N/A